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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/894,334	06/27/2001	Qing Ma	42390P10606	6477
8791	7590 03/01/2004		EXAMINER	
BLAKELY SOKOLOFF TAYLOR & ZAFMAN 12400 WILSHIRE BOULEVARD, SEVENTH FLOOR			COLEMAN, WILLIAM D	
	ES, CA 90025	VENTITEOOR	ART UNIT	PAPER NUMBER
	•		2823	
			DATE MAILED: 02/01/200	4

Please find below and/or attached an Office communication concerning this application or proceeding.

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	Application No.	Applicant(s)				
0.00	09/894,334	MA ET AL.				
Office Action Summary	Examiner	Art Unit				
	W. David Coleman	2823				
The MAILING DATE of this communication ap Period for Reply	pears on the cover sheet with t	he correspondence address				
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1. after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a rep - If NO period for reply is specified above, the maximum statutory period - Failure to reply within the set or extended period for reply will, by statut Any reply received by the Office later than three months after the mailin earned patent term adjustment. See 37 CFR 1.704(b).		be timely filed O) days will be considered timely. S from the mailing date of this communication. DONED (35 U.S.C. § 133).				
Status						
1) Responsive to communication(s) filed on 29 .	January 2004.					
,	☐ This action is FINAL . 2b) ☐ This action is non-final.					
•	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims						
4) Claim(s) 1,3-5,7,8 and 10-12 is/are pending in 4a) Of the above claim(s) is/are withdra 5) Claim(s) is/are allowed. 6) Claim(s) 1,3-5,7,8 and 10-12 is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and/	awn from consideration.					
Application Papers						
9) The specification is objected to by the Examin 10) The drawing(s) filed on is/are: a) ac Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct	cepted or b) objected to by e drawing(s) be held in abeyance ction is required if the drawing(s)	. See 37 CFR 1.85(a). is objected to. See 37 CFR 1.121(d).				
11) The oath or declaration is objected to by the E		Ance Action of John 1 10-102.				
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreig a) All b) Some * c) None of: 1. Certified copies of the priority documer 2. Certified copies of the priority documer 3. Copies of the certified copies of the priority documer application from the International Burea * See the attached detailed Office action for a list	nts have been received. nts have been received in Apporting documents have been reau (PCT Rule 17.2(a)).	lication No ceived in this National Stage				
Attachment(s) 1) Mily Notice of References Cited (RTO 892)	4) ⊠ Interview Sun	omary (PTO-413)				
 Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-948) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08 Paper No(s)/Mail Date 	Paper No(s)/N	Mail Date rmal Patent Application (PTO-152)				

DETAILED ACTION

Response to Arguments

1. Applicant's arguments, see Interview Summary form, filed herewith, with respect to claims 1, 3, 4, 5, 7, 8, 10, 11 and 12 have been fully considered and are persuasive. The rejection of the previous Office Action has been withdrawn.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 3. Claims 1, 3, 4, 5, 7, 8, 9,10, 11 and 12 are rejected under 35 U.S.C. 102(b) as being anticipated by Chen et al., U.S. Patent 5,399,415.
- 4. See **FIGS. 1-18** where <u>Chen</u> discloses a method of manufacturing a semiconductor.
- 5. Pertaining to claim 1, Chen discloses a method comprising:

 over an area of a substrate 10, forming a plurality of three dimensional first structures 20, 21, 22 and 23;

following forming the first structures, conformally introducing a sacrificial material 14 & 16(as seen in FIGS. 1 & 2) over the area of the substrate including on a portion of the plurality of first structures;

introducing a second structural material 66 over the sacrificial material; exposing a portion of the sacrificial material (as seen in 4A & 5A); removing the sacrificial material; and

Application/Control Number: 09/894,334 Page 3

Art Unit: 2823

wherein removing the sacrificial material comprises suspending the second structural material as a second structure electrically coupled to the first structure (please note that the tungsten elements are suspended and electrically coupled, see Abstract).

- 6. Pertaining to claim 3, <u>Chen</u> discloses the method of claim 1, wherein exposing a portion of the sacrificial material comprises removing a portion of the second structural material.
- 7. Pertaining to claim 4, <u>Chen</u> discloses the method of claim 1, prior to introducing the second structural material, further comprising patterning the sacrificial material (Chen utilizes a photoresist).
- 8. Pertaining to claim 5, <u>Chen</u> discloses the method of claim 1, wherein the first structural material comprises silicon and the sacrificial material comprises silicon dioxide and introducing the sacrificial material comprises growing.
- 9. Pertaining to claim 7, <u>Chen</u> discloses the method of claim 1, wherein patterning the first structures defines a plurality of first areas of the portion of the substrate occupied by the first structures and at least a second area of the portion of the substrate free of the first structures, and introducing the sacrificial material comprises introducing the sacrificial material at least over the second area (please note the undercutting in **FIGS. 6 & 6A**).

Page 4

Art Unit: 2823

10. Pertaining to claim 8, Chen teaches a method comprising:

over an area of a surface of a substrate 10, lithographically patterning (column 5, lines 26-63) a plurality of first structures, the plurality of first structures having a first dimension about the surface (i.e., the lateral separation of the top surface) of the substrate and a second different dimension;

following forming the plurality of first structures, conformally introducing a sacrificial material layer over the area of the substrate including on a portion of the plurality of first structures;

patterning the sacrificial material;

forming second structures over the sacrificial material;

removing the sacrificial material; and

wherein removing the sacrificial material comprises suspending the second structure by the first structure, and wherein the second structure is electrically coupled to the first structure (see Abstract, where Chen teaches the above limitations in which the second structure is electrically coupled and suspended).

- 11. Pertaining to claim 10, <u>Chen</u> teaches the method of claim 8, prior to removing the sacrificial material, further comprising exposing a portion of the sacrificial material.
- 12. Pertaining to claim 11, <u>Chen</u> teaches the method of claim 10, wherein exposing a portion of the sacrificial material comprises removing a portion of the second structural material.

Application/Control Number: 09/894,334

Page 5

Art Unit: 2823

13. Pertaining to claim 12, <u>Chen</u> teaches the method of claim 8, wherein the sacrificial material comprises silicon dioxide and introducing the sacrificial material comprises growing.

Conclusion

- 14. Any inquiry concerning this communication or earlier communications from the examiner should be directed to W. David Coleman whose telephone number is 571-272-1856.

 The examiner can normally be reached on 9:00 AM-5:00 PM.
- 15. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Olik Chaudhuri can be reached on 703-306-2794. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.
- Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

W. David Coleman Primary Examiner Art Unit 2823